

IN THE CLAIMS:

Please cancel Claims 38, 45 and 46 without prejudice or disclaimer of the subject matter recited therein.

Please amend Claim 32 and add new Claims 47-49 as follows.

Claims 1-31. (Cancelled).

32. (Currently Amended) An image display apparatus comprising:
a plurality of display devices wired in a matrix through a plurality of scanning signal wirings and a plurality of modulated signal wirings; and
a driving circuit configured to apply a modulated signal having a modulated pulsewidth to each of said plurality of modulated signal wirings,
wherein said driving circuit has a plurality of charge paths connected to each of the plurality of modulated signal wirings, wherein the plurality of charge paths are connected in parallel to each of the plurality of modulated signal wirings, and the number of charge paths that are driven is changed in a time period for applying one modulated signal to the one display device for changing a signal level of the modulated signal from a first level corresponding to a display state to a second level corresponding to a non-display state, and changes an operating state of each of the plurality of charge paths in the process of changing the signal level of the modulated signal from the first level to the second level.

33. (Previously Presented) An image display apparatus comprising:
a plurality of display devices wired in a matrix through a plurality of scanning signal wirings and a plurality of modulated signal wirings; and
a driving circuit configured to apply a modulated signal having a modulated pulsewidth to each of said plurality of modulated signal wirings,
wherein a signal level of the modulated signals corresponding to substantially all luminance which are designated by image signals is changed from a level corresponding to a display state to a level corresponding to a non-display state via an intermediate level, wherein the modulated signals corresponding to substantially all luminance do not include a modulated signal corresponding to the image signal for designating the non-display state.

34. (Previously Presented) The apparatus according to claim 33, wherein said driving circuit has a plurality of charge paths for changing the signal level of the modulated signal, and at least one of the plurality of charges paths is connected to a predetermined potential.

35. (Previously Presented) The apparatus according to claim 32, wherein at least one of the plurality of charge paths is connected to a predetermined potential.

36. (Previously Presented) The apparatus according to claim 34, wherein the plurality of charge paths have different change amounts per unit time of the signal level when the signal level is to fall.

37. (Previously Presented) The apparatus according to claim 36, wherein the operation states of the plurality of charge paths are changed by exclusively operating charge paths having different charge amounts per unit time of the signal level of the modulated signal when the signal level of the modulated signal is to fall.

Claim 38. (Cancelled).

39. (Previously Presented) The apparatus according to claim 34, further comprising a circuit for determining the operation states of the plurality of charge paths.

40. (Previously Presented) The apparatus according to claim 32, wherein said driving circuit comprises a rise circuit for raising the signal level of the modulated signal and a separate fall circuit for causing the signal level of the modulated signal to fall.

41. (Previously Presented) The apparatus according to claim 32, wherein each said display device comprises an electron-emitting device.

Claims 42 and 43. (Cancelled).

44. (Previously Presented) An image display apparatus comprising:
a plurality of display devices wired in a matrix through a plurality of scanning signal wirings and a plurality of modulated signal wirings; and
a driving circuit configured to apply a modulated signal having a modulated pulsewidth to each of the plurality of modulated signal wirings,
wherein said driving circuit has a plurality of charge paths for changing a signal level of the modulated signal from a first level corresponding to a display state to a second level corresponding to a non-display state, and changes the number of the charge paths being operated in the process of changing the signal level of the modulated signal from the first level to the second level, wherein charge paths are operated in parallel in a period where some or all charge paths of the plurality of charge paths are operated.

Claims 45 and 46. (Cancelled).

47. (New) An image display apparatus, comprising:
a plurality of display devices wired in a matrix through a plurality of scanning signal wirings and a plurality of modulated signal wirings; and
a driving circuit configured to apply a pulse signal as a modulated signal having a modulated pulsewidth to each of the plurality of modulated signal wirings,

wherein at least one pulse signal has a first portion having a signal level to be lower than a maximum level of the pulse signal at the leading edge of the pulse signal, and a second portion having a signal level to be lower than the maximum level at the trailing edge of the pulse signal.

48. (New) An apparatus according to claim 47, wherein the first portion is created by said driving circuit maintaining the signal level of the pulse signal to be lower than the maximum level for a predetermined time period.

49. (New) An apparatus according to claim 47, wherein the signal level of the first and second portions is at the approximate level of an operation threshold level of the display device.